

CMS Barrel Drift Tubes

Neutron Irradiation Tests of Chamber Electronics

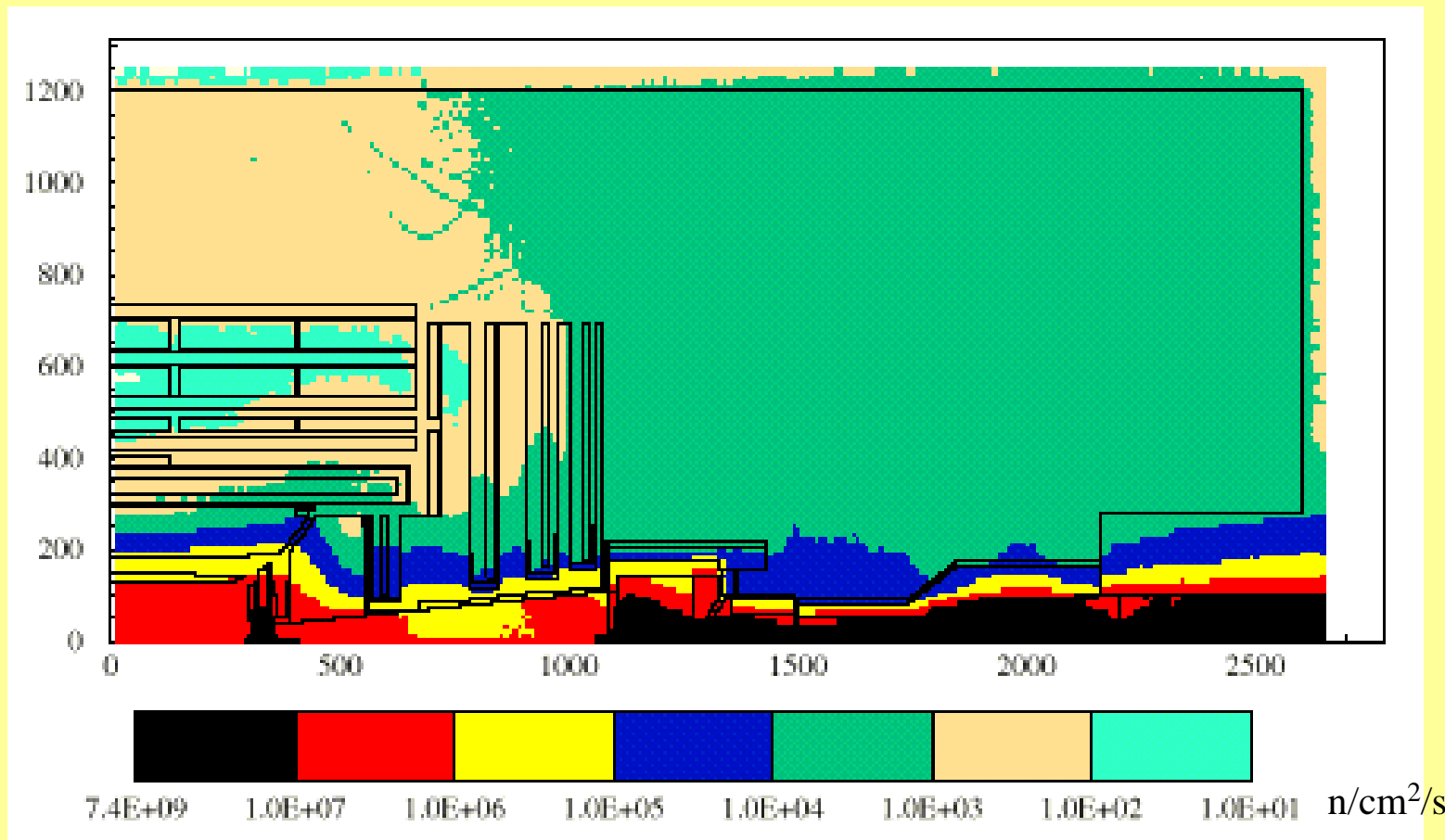
Experimental setup and test results

S. Agosteo, L.Castellani, I. Lippi, R. Martinelli, P. Zotto



CMS Neutron Environment

Neutron fluxes in the CMS area

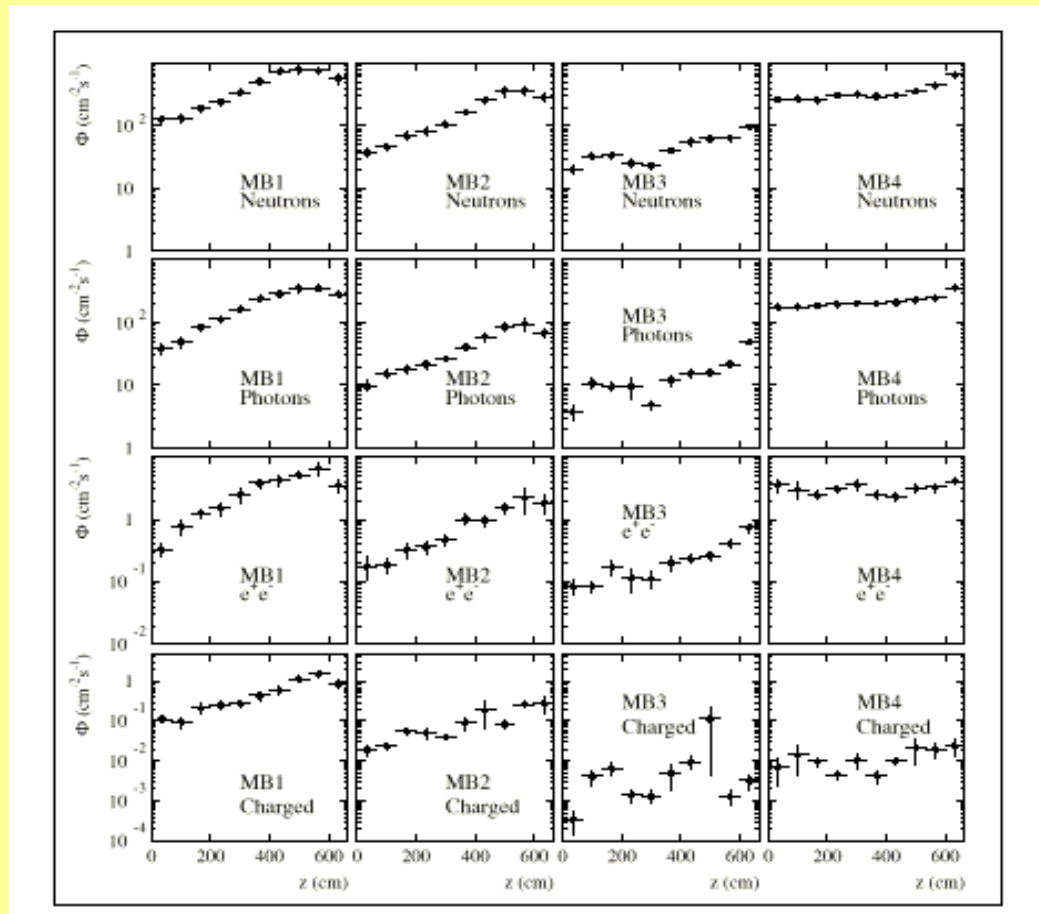


LHC luminosity = $10^{34} \text{ cm}^{-2} \text{ s}^{-1}$



CMS Neutron Environment

Neutron fluxes in the barrel muon chambers

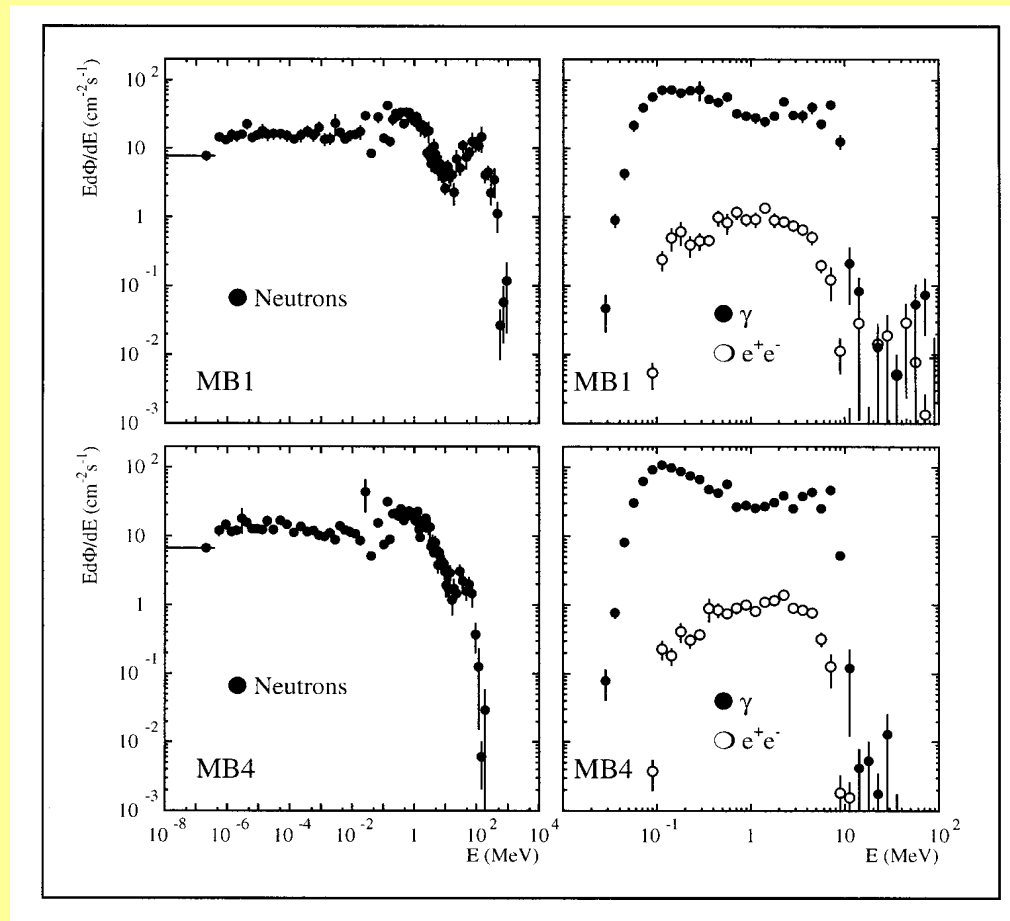


LHC luminosity = $10^{34} \text{ cm}^{-2} \text{ s}^{-1}$



CMS Neutron Environment

Particle spectra in the barrel muon chambers

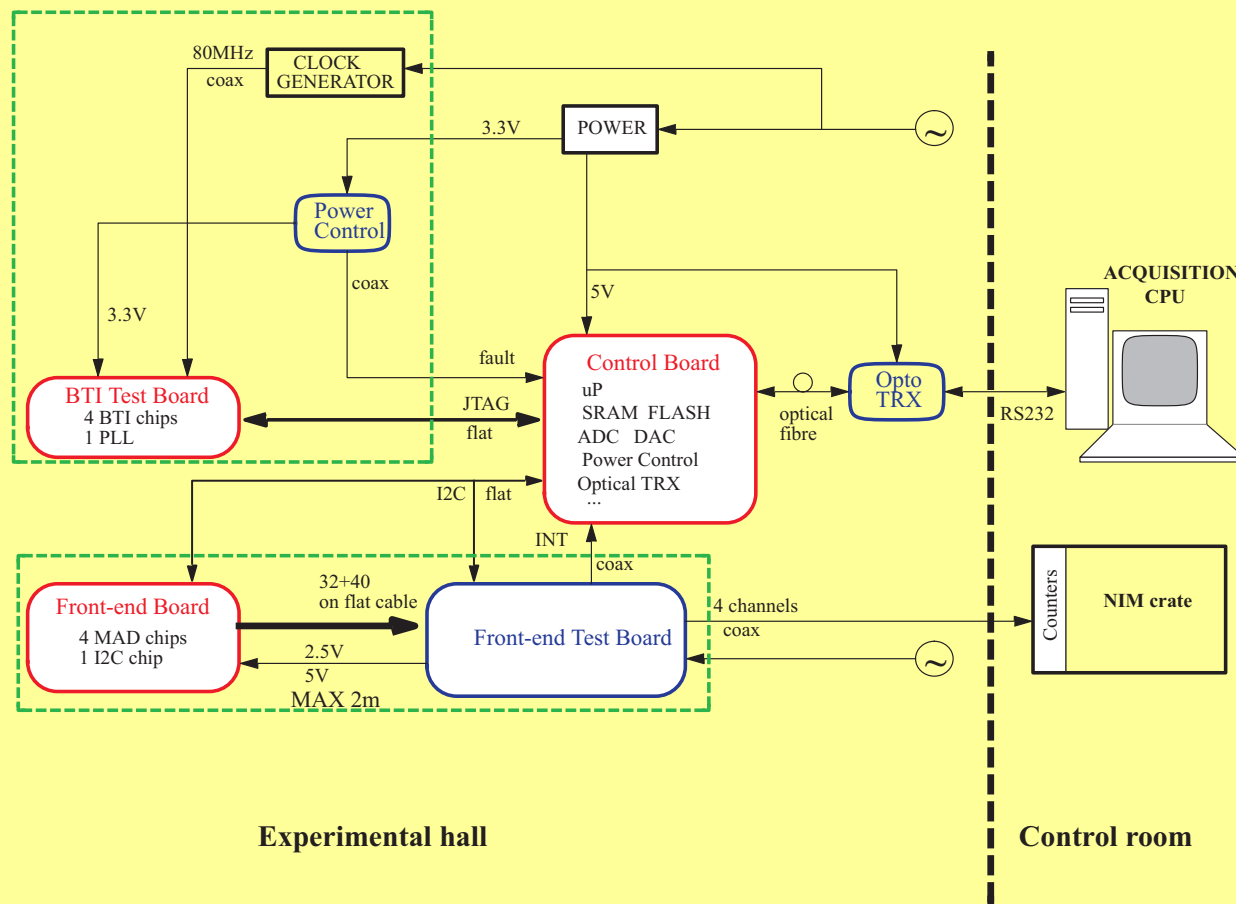


LHC luminosity = $10^{34} \text{ cm}^{-2} \text{ s}^{-1}$



Neutron Irradiation Test 1999

Acquisition setup for SEE characterization





Neutron Irradiation Test 06/99

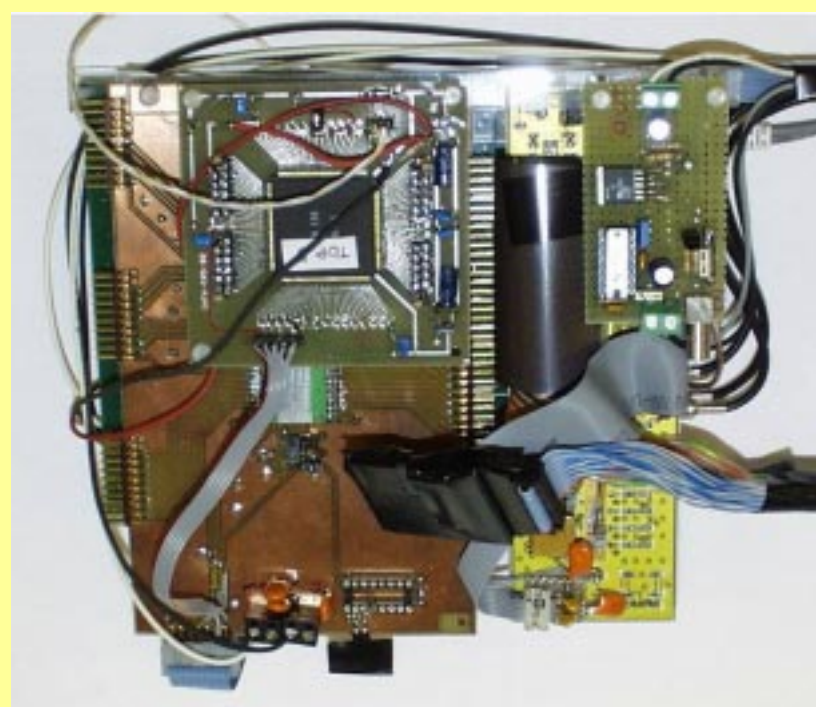
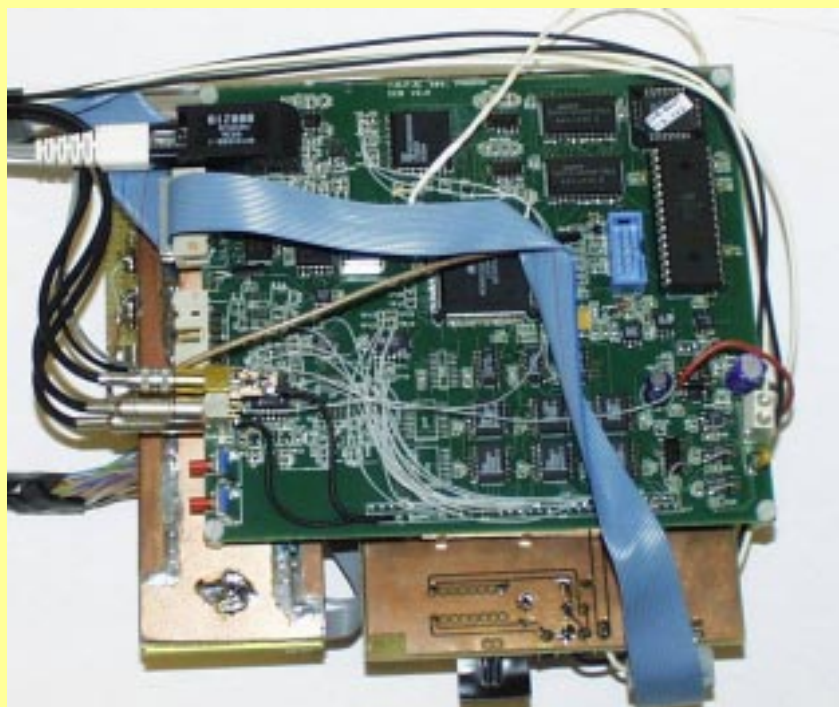
DAQ Setup





Neutron Irradiation Test 06/99

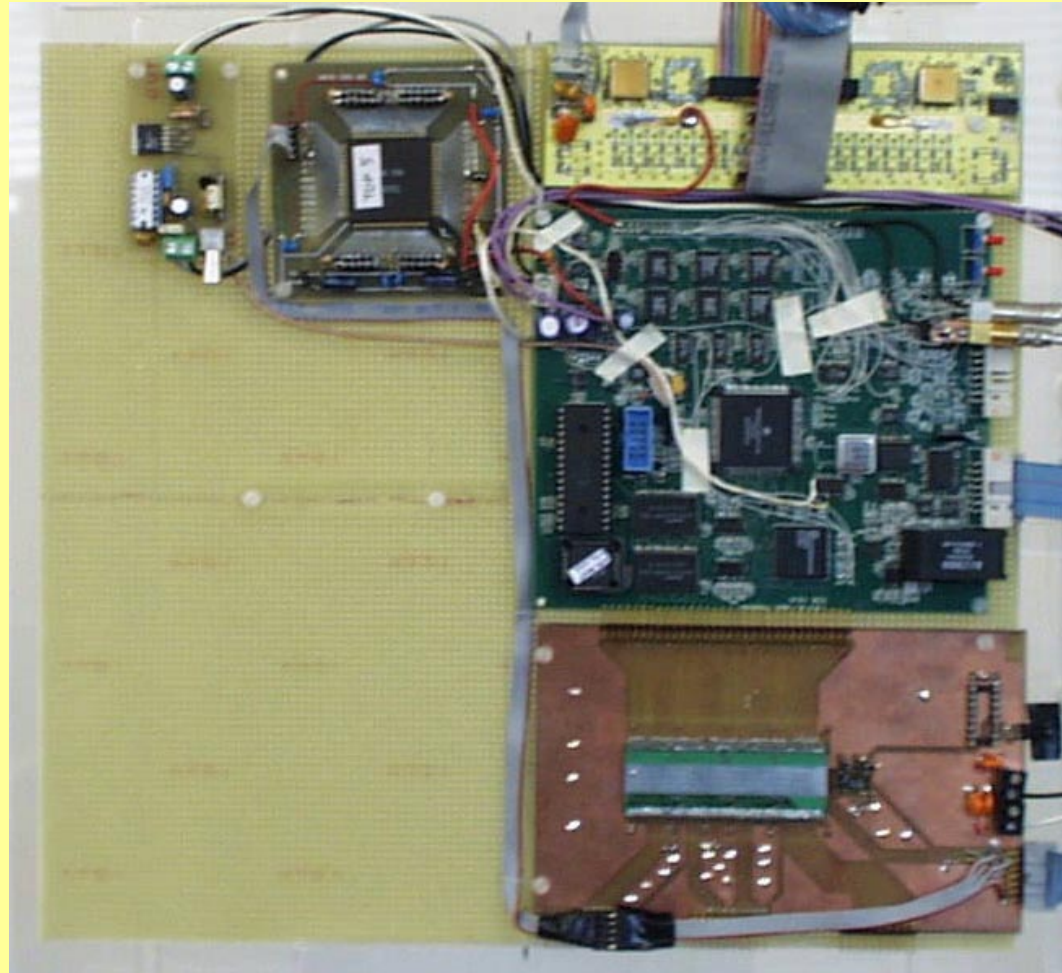
Thermal neutron irradiation DUT





Neutron Irradiation Test 06/99

Fast neutron irradiation DUT





Neutron Irradiation Test 06/99

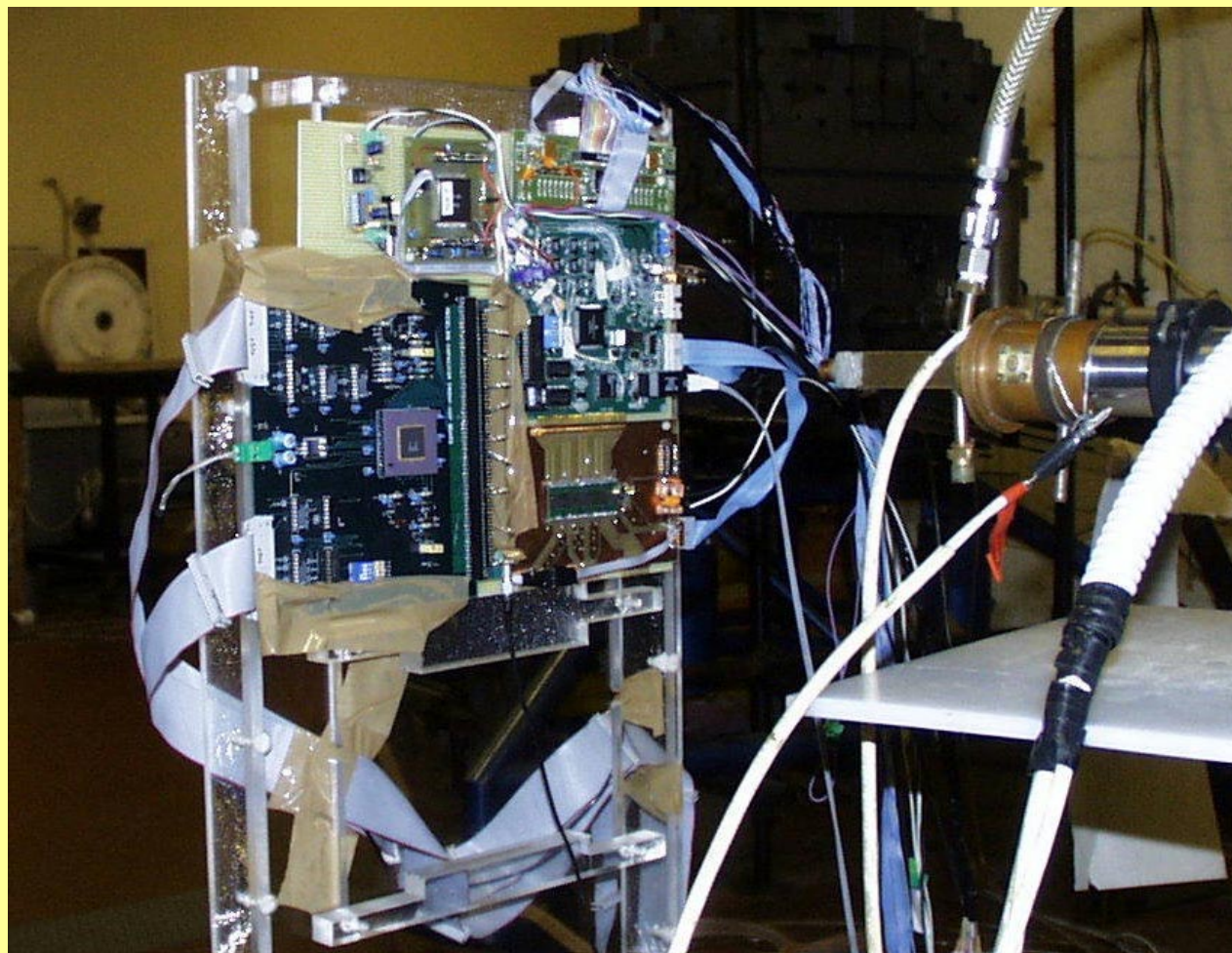
Thermal neutrons experimental setup





Neutron Irradiation Test 06/99

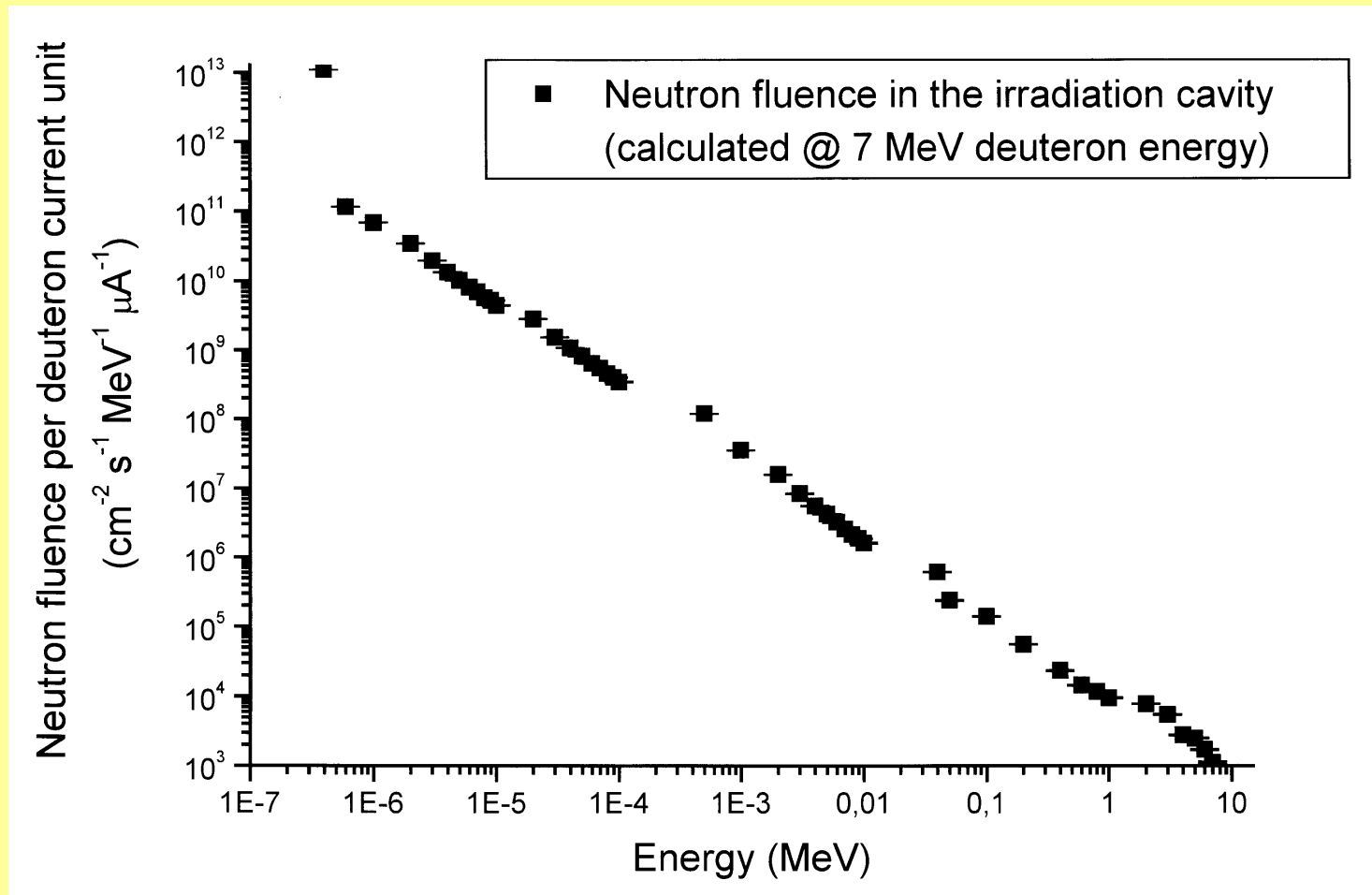
Fast neutrons experimental setup





Neutron Irradiation Test 06/99

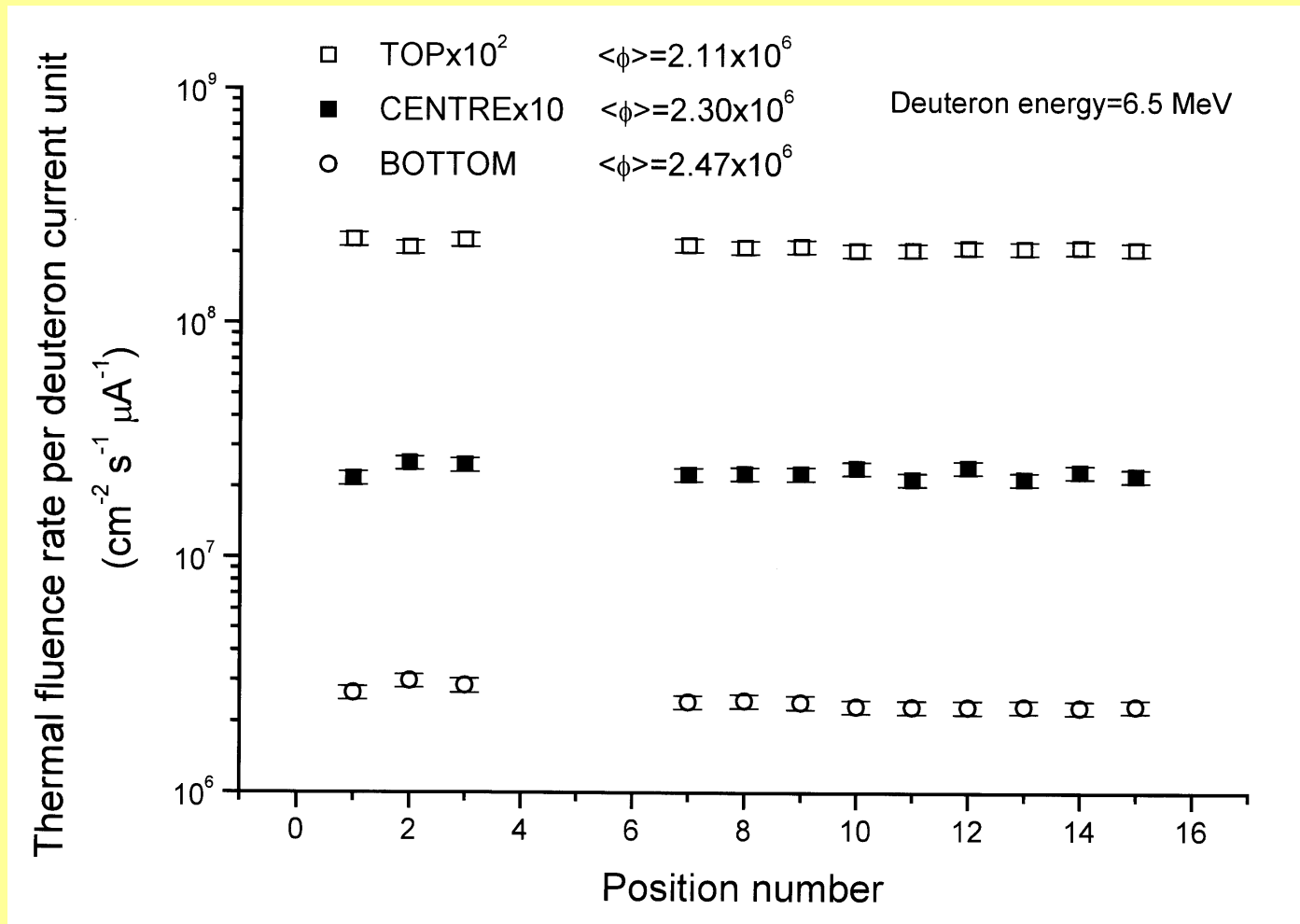
Thermal neutron spectrum





Neutron Irradiation Test 06/99

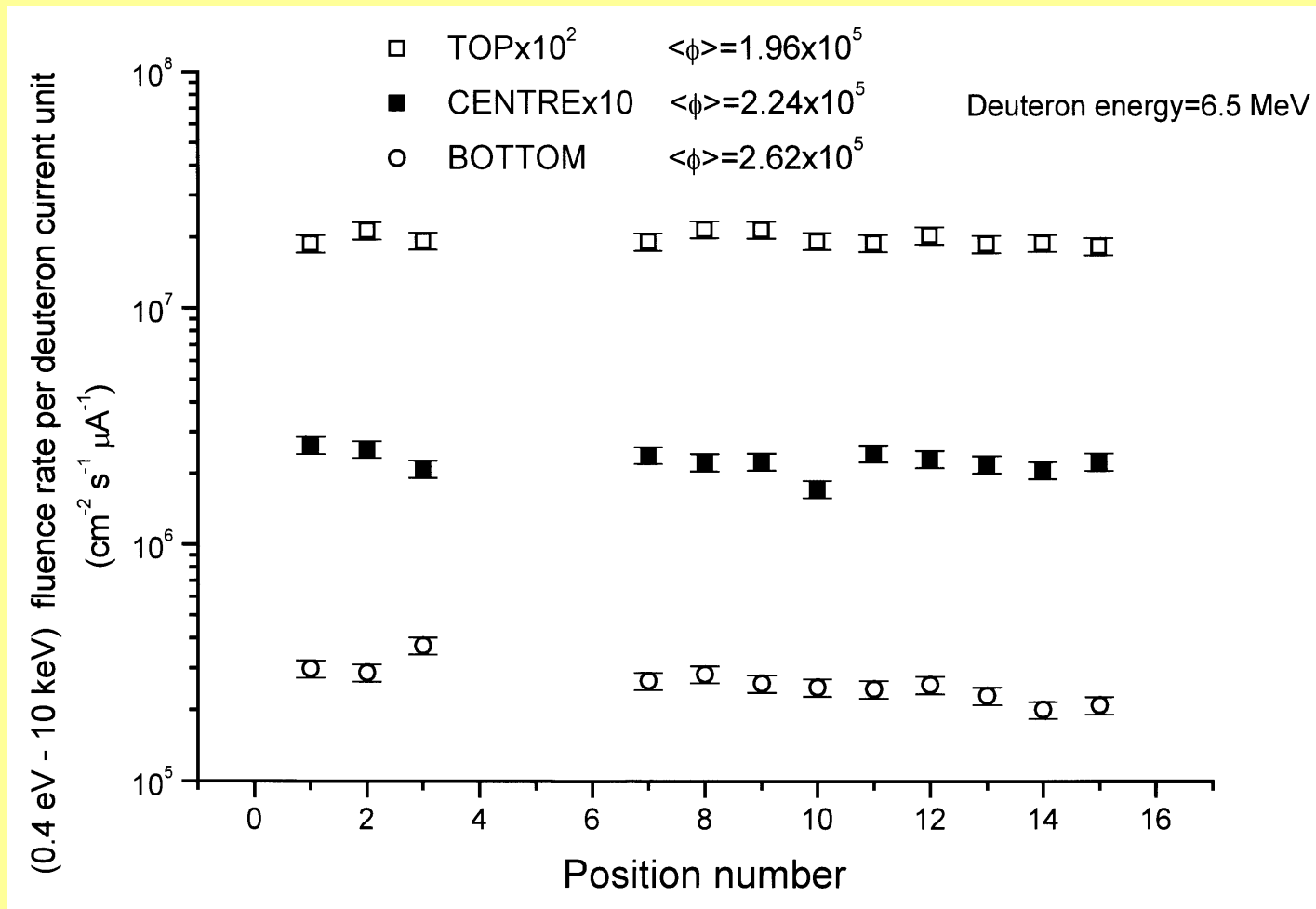
Thermal neutron fluence in irradiation cavity





Neutron Irradiation Test 06/99

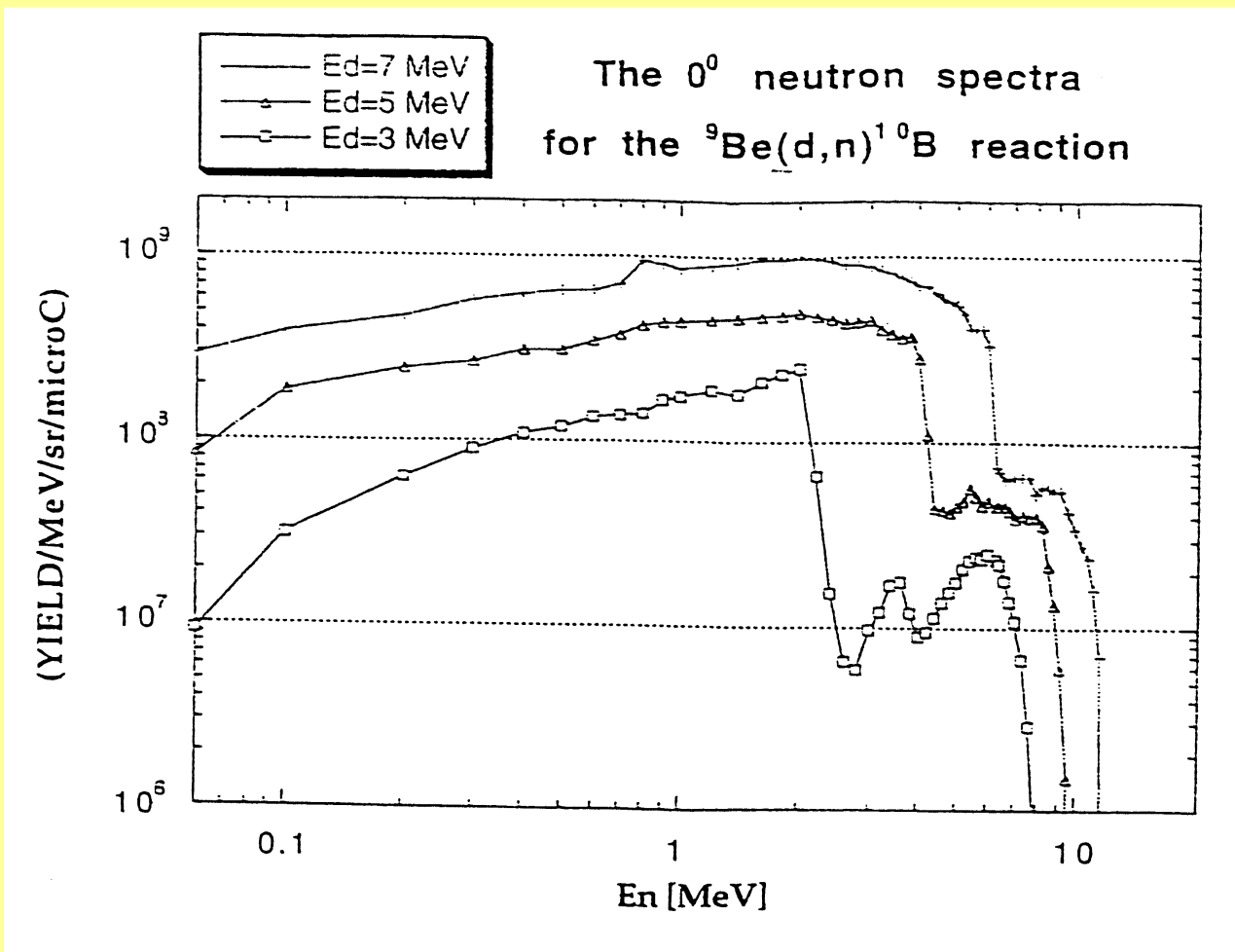
Epithermal neutron fluence in irradiation cavity





Neutron Irradiation Test 06/99

Fast neutrons spectra



Neutron Irradiation Tests

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Reaction ${}^9\text{Be} + d \rightarrow {}^{10}\text{B} + n$ (d @ 6.5MeV)

Thermal neutrons obtained using a heavy water moderator and a graphite reflector.

Expected neutron rate at CMS is 1000 (n/cm²s⁻¹) of which 50% is thermal and 50% is fast neutrons.

Tested components

Component	Productor / Type / Year
LD Regulator	MICREL / 29501-3.3BU / 1997
μP	MOTOROLA / MC68HC16 / 1994
FLASH	ATMEL / AT29C101A-12PC / 1996
SRAM#1	SONY / CXK581000AM-70LL / 1993
SRAM#2	SONY / CXK581000AM-70LL / 1993
EPROM	ATMEL / AT27C512R-15JC / 1995
Optical transceiver	HONEYWELL/ HFM2600-1 / 1998
ASIC TSS	ES2 0.7 μm / TOP5 ceramic package / 1997
BTI	ATMEL 0.5 μm / LTCC substrate & in dies /1997
MAD	AMS 0.8 μm / BCMOS / 1997

Results with thermal neutrons

Component	Total rate n/cm ²	Device SEU probability SEU/(n*cm ²)	Mean time between failures in the full detector hh:mm
LD Regulator	$3.54 \cdot 10^{10}$	$< 2.68 \cdot 10^{-10}$	> 33:09
μP	$3.54 \cdot 10^{10}$	$< 2.68 \cdot 10^{-10}$	> 198:51
FLASH	$3.54 \cdot 10^{10}$	$< 2.68 \cdot 10^{-10}$	> 198:51
SRAM#1	$3.54 \cdot 10^{10}$	$1.35 \cdot 10^{-9}$	19:49
SRAM#2	$3.54 \cdot 10^{10}$	$< 2.68 \cdot 10^{-10}$	> 99:25
EPROM	$3.54 \cdot 10^{10}$	$< 2.68 \cdot 10^{-10}$	> 198:51
Optical transceiver	$3.54 \cdot 10^{10}$	$< 2.68 \cdot 10^{-10}$	> 198:51
ASIC TSS	$2.36 \cdot 10^{10}$	$< 2.68 \cdot 10^{-10}$	> 16:42
BTI	$2.36 \cdot 10^{10}$	$< 2.68 \cdot 10^{-10}$	> 00:59
MAD	$9.10 \cdot 10^9$	See dedicated slides	

Confidence level is 90%

Particular events

One LD regulator SEU happened after $7 \cdot 10^9$ n/cm² integrated flux

Assuming they were due to a true LD regulator fault we should have

SEU probability $2.82 \cdot 10^{-11}$ SEU/(n*cm²)
 Mean Time Between Failures 314:40 hh:mm

Results with fast neutrons

Component	Total rate n/cm ²	Device SEU probability SEU/(n*cm ²)	Mean time between failures in the full detector hh:mm
LD Regulator	1.27*10 ¹²	< 7.50*10 ⁻¹²	> 1185:34
μP	1.27*10 ¹²	< 7.48*10 ⁻¹²	> 7128:16
FLASH	1.21*10 ¹²	< 7.83*10 ⁻¹²	> 6811:20
SRAM#1	7.49*10 ¹¹	2.82*10 ⁻¹⁰	94:29
SRAM#2	1.52*10 ¹²	1.75*10 ⁻¹²	19019:27
EPROM	1.10*10 ¹²	< 8.59*10 ⁻¹²	> 6206:52
Optical transceiver	1.24*10 ¹²	< 7.65*10 ⁻¹²	> 6970:36
ASIC TSS	1.87*10 ¹²	< 5.06*10 ⁻¹²	> 866:42
BTI	1.35*10 ¹²	< 7.03*10 ⁻¹²	> 37:56
MAD	6.30*10 ¹⁰	see dedicated slides	

Confidence level is 90%

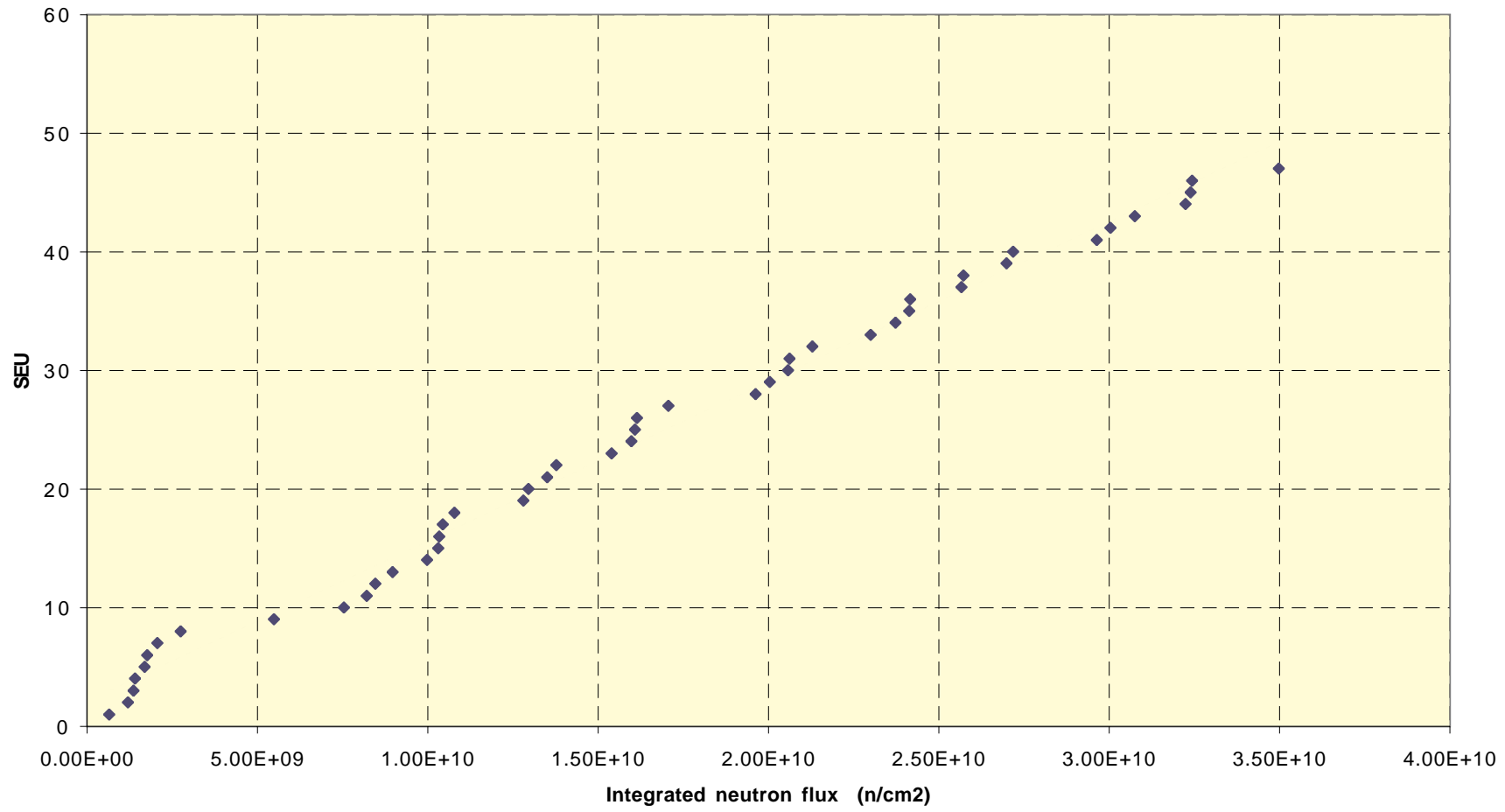
Particular events

Two microprocessor reboot happened

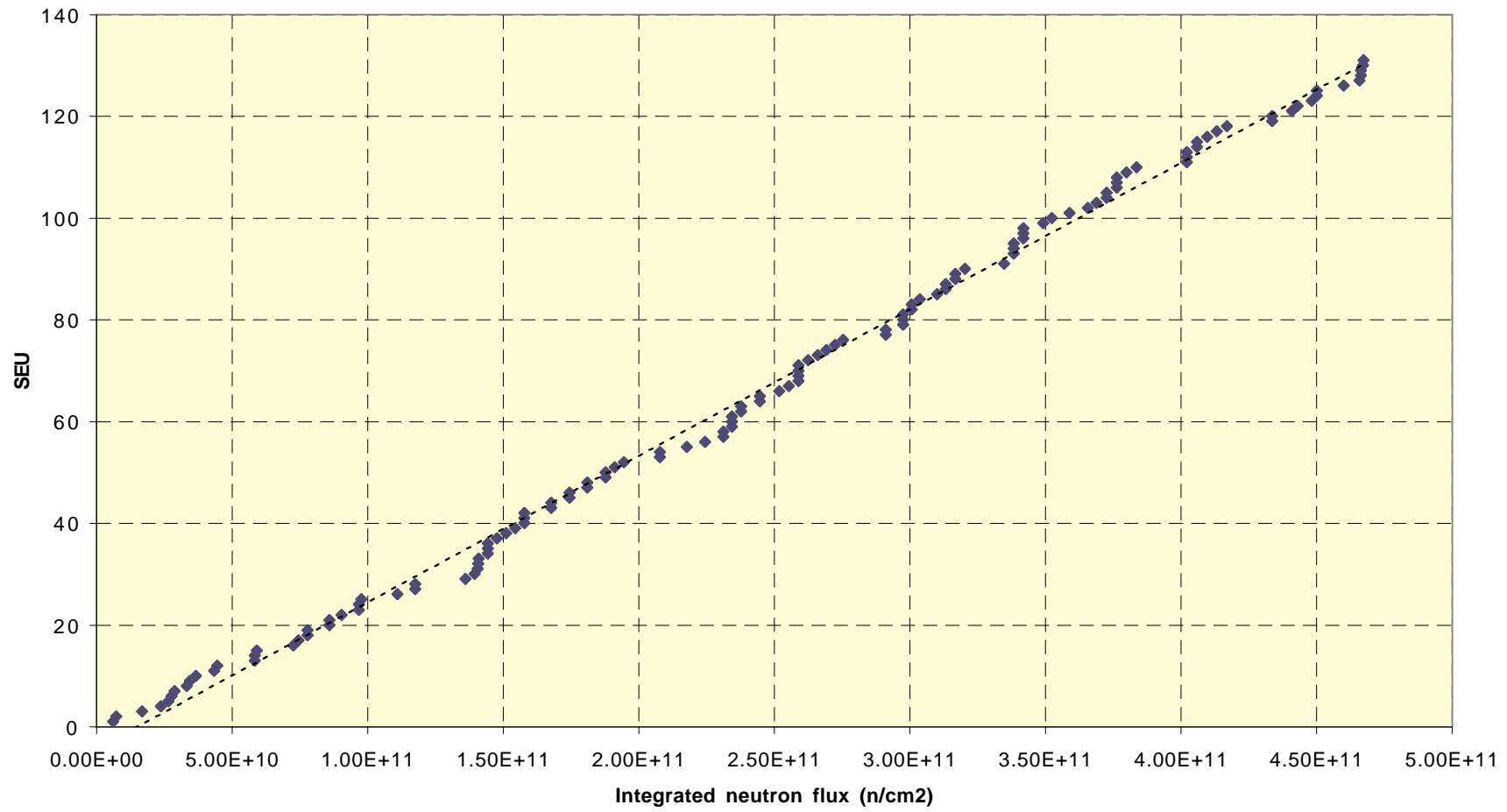
We could not uniquely identify the problem (program corrupted in RAM, I/O lines upset,...). Assuming they were due to a true microprocessor fault we should have

SEU probability $1.07 \cdot 10^{-12}$ SEU/(n*cm²)
 Mean Time Between Failures 12498:30 hh:mm

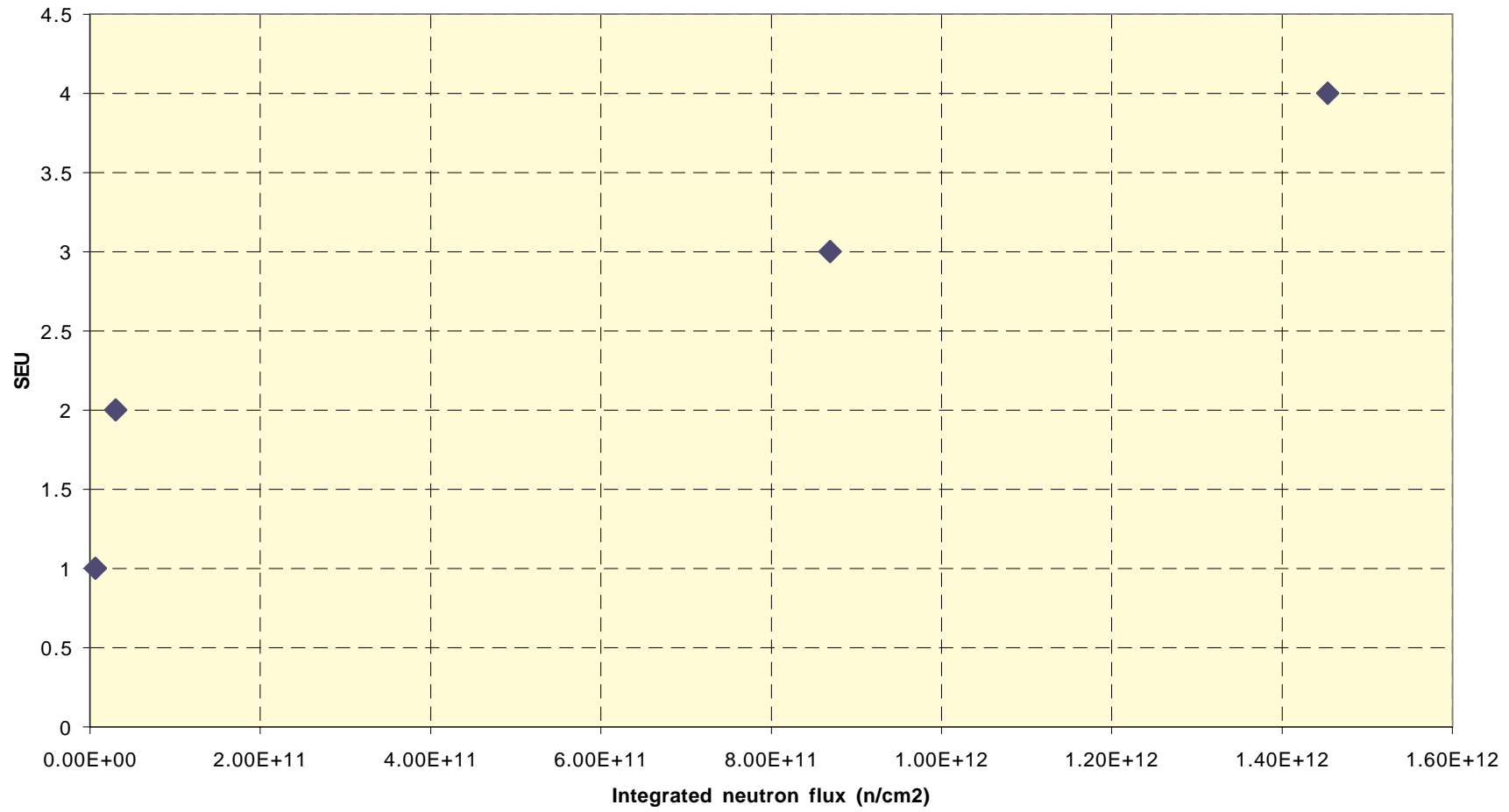
Thermal neutrons induced SEU on SRAM #1



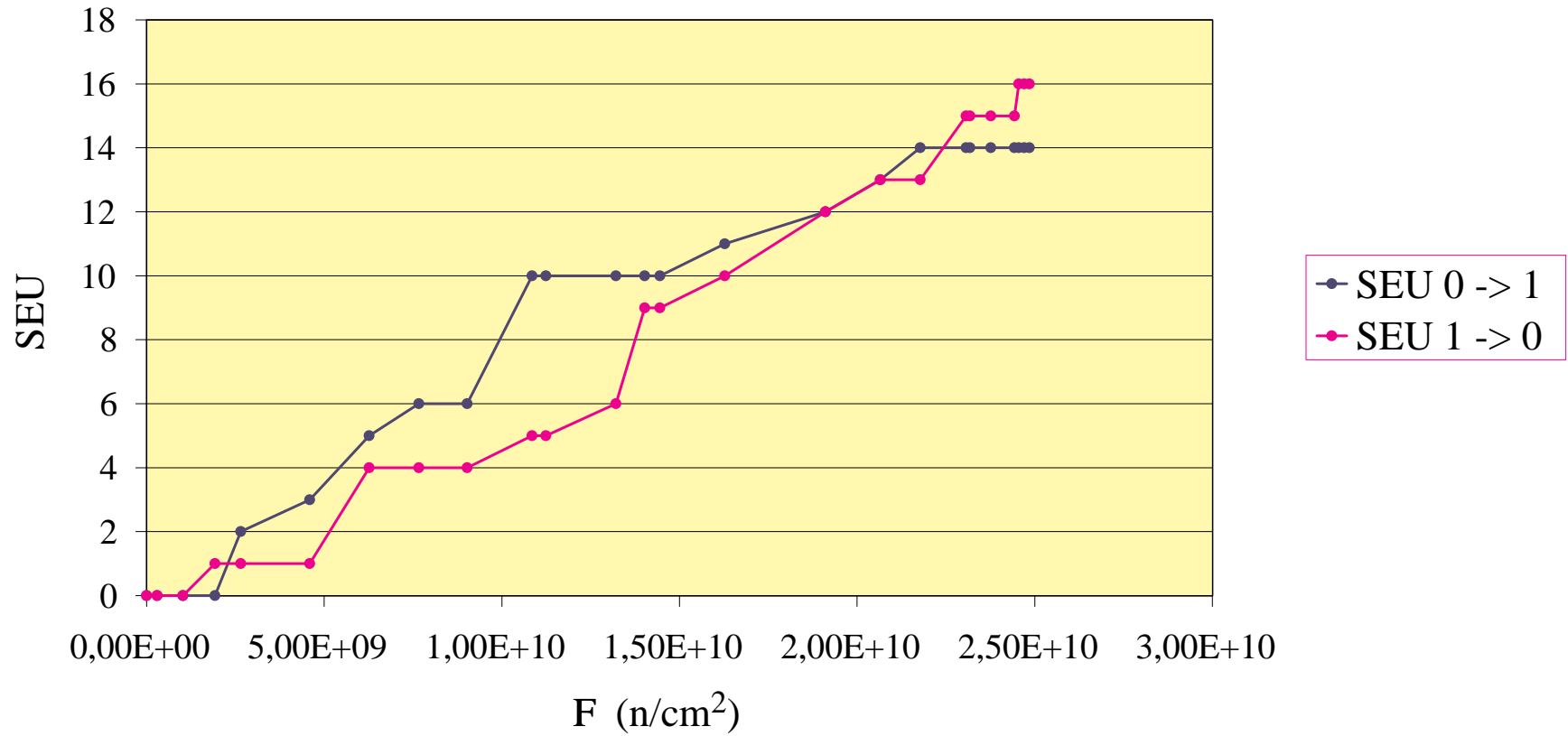
Fast neutrons induced SEU on SRAM#1



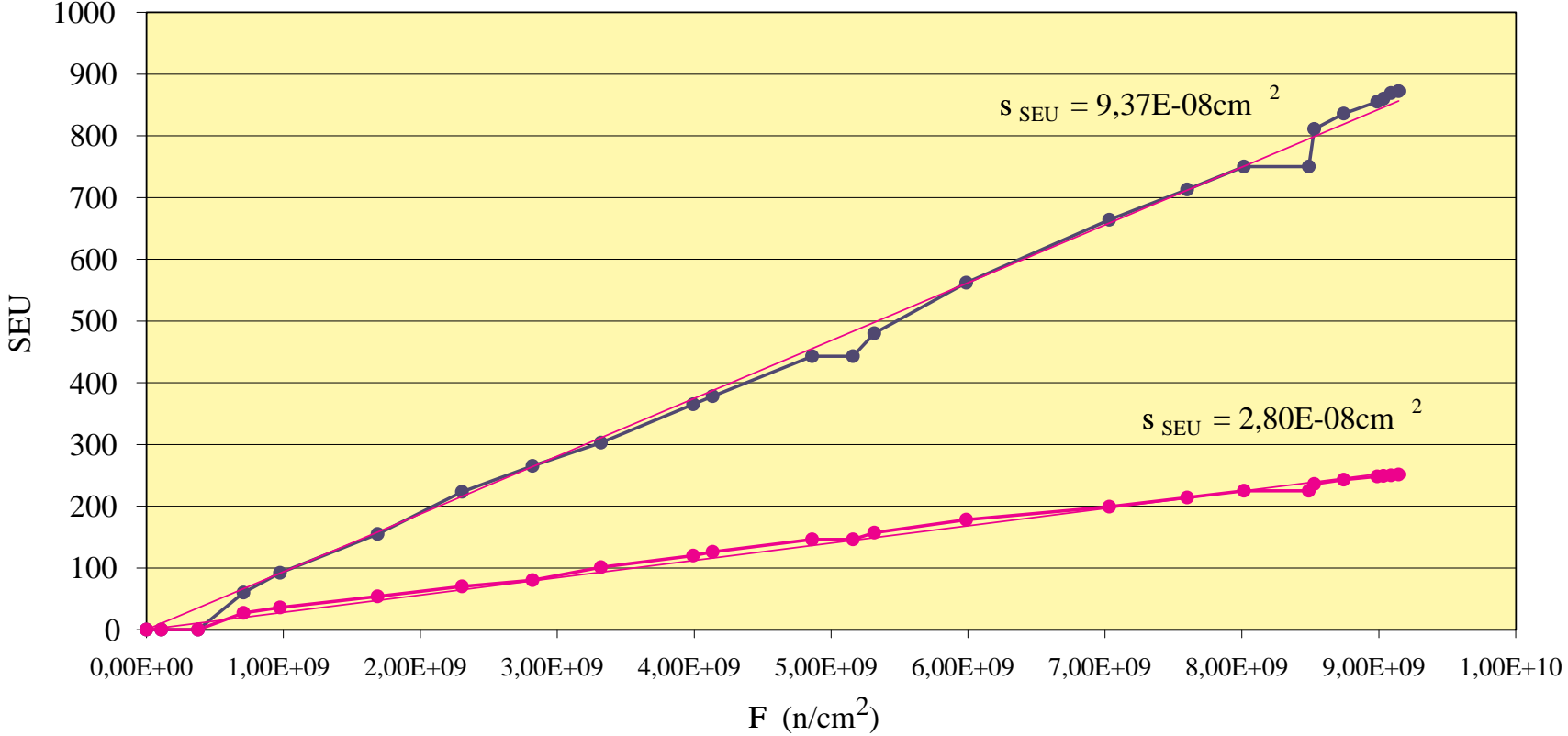
Fast neutrons induced SEU on SRAM#2



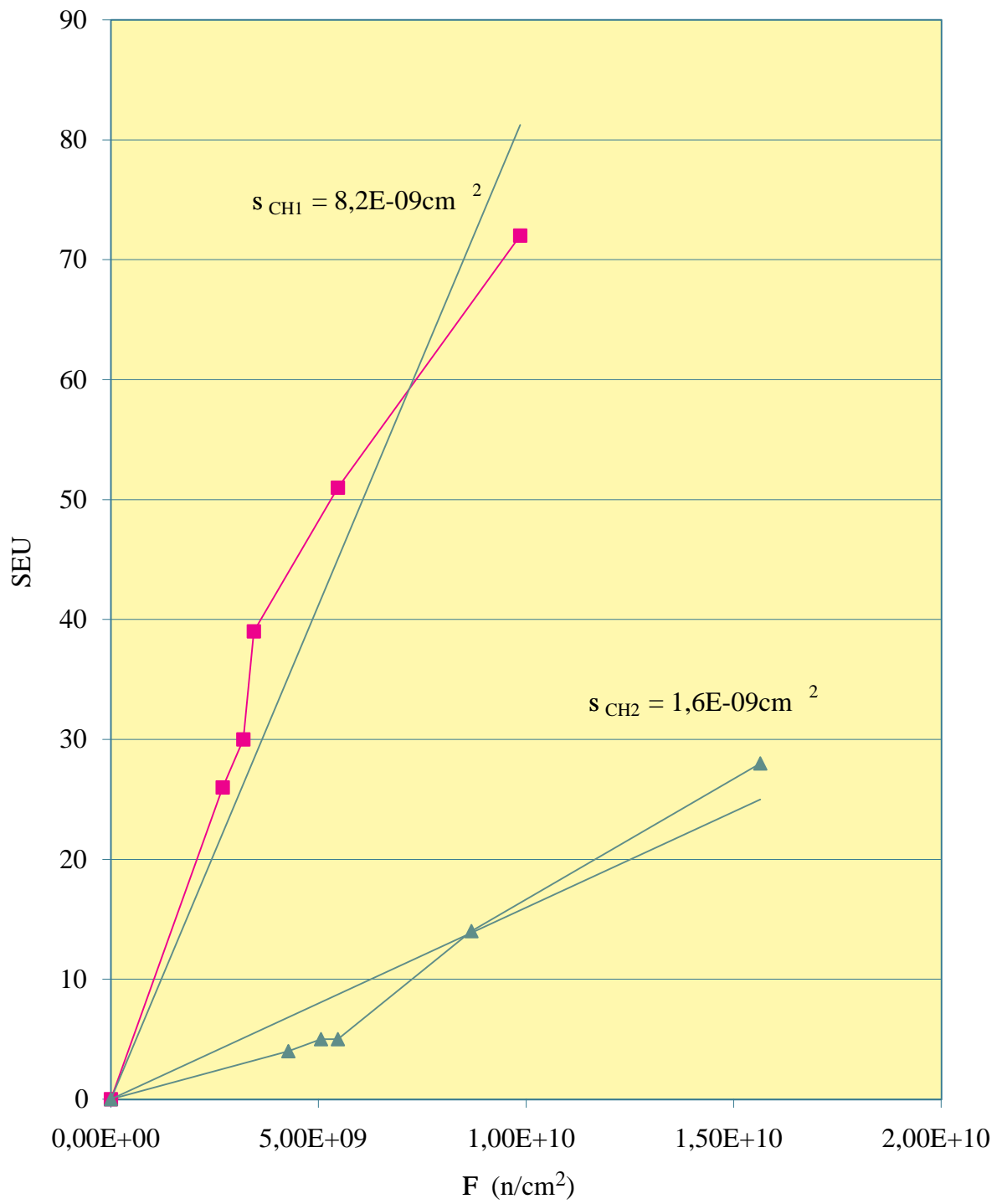
Thermal neutrons induced SEU on SRAM#1 06/99



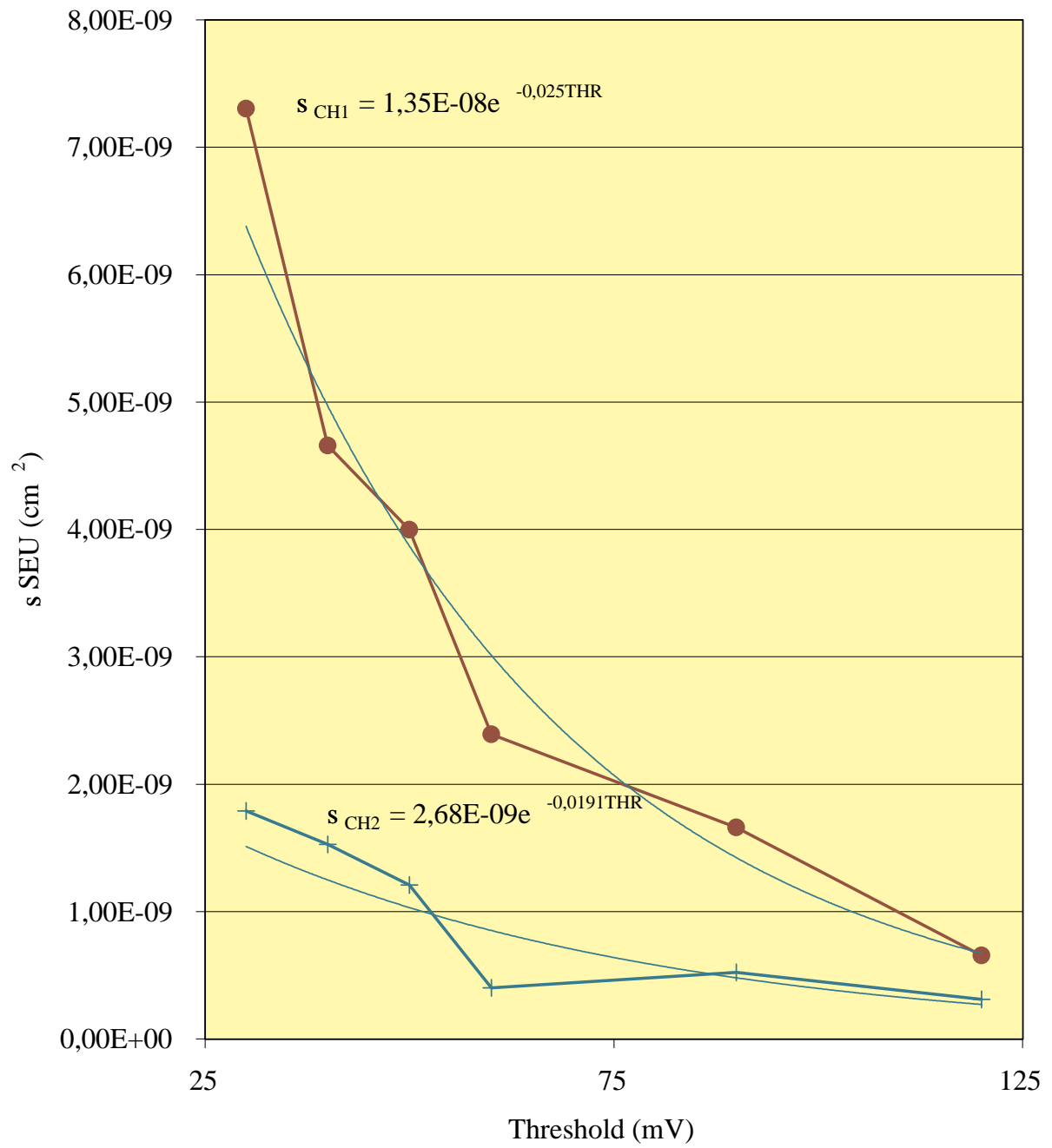
Thermal neutrons induced SEU on MAD @ thr=60mV



Fast Neutrons induced SEU on MAD @ thr=30mV



MAD SEU cross-section versus threshold



Radiation damage verification

As a test of the radiation damage caused by the neutron interactions with the devices we compared the standby current of the devices

Device	Current before irradiation	Current after irradiation
Control board	220 mA	222 mA
TSS	3.1 mA	3.4 mA
LD regulator	12.7 mA	12.7 mA
BTI	4.7 mA	4.7 mA

The Flash memory can still be reprogrammed

The EPROM is still working with $V_{cc} = \text{HIGH, LOW, STD}$

The levels of optical link are unchanged

The ADC channels are still working correctly

Only the TSS shows an indication of damage, but it will be remade using 0.5 μm technology