

Wedge Sorter output data format (2004 testbeam)

The LVDS output signals from the Wedge Sorter are converted into TTL by using an adapter board. The output of the adapter is on 2 flat cables that can be plugged into input fifos of a Pattern Unit: one cable for each muon selected by the Wedge Sorter. Since we have only one PHTF, the WS is programmed in such a way that it forwards the two muon candidates found by the same PHTF.

The meaning of the input bits to the Pattern Unit for each cable is:

Bit	Parameter	Comments
30-28	Quality	Quality of muon candidate as reconstructed by PHTF
27-23	Pt	Transverse momentum as reconstructed by PHTF
22	Charge	Sign of the bending
21-14	Phi	Position as reconstructed by PHTF
13-7	Eta	Eta: <i>not used in test beam</i>
6-0	Address	Information about the stations used for extrapolation. <i>Not relevant for the test beam.</i>